

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	20	(US-20040025069-\$ or US-20030084269-\$ or US-20050033831-\$).did. or (US-7174468-\$ or US-6976155-\$ or US-6105053-\$).did. or (WO-2005010737-\$ or US-20050022038-\$ or US-20050013705-\$ or US-20040215987-\$ or US-20040037346-\$ or US-20040249588-\$ or US-20030005400-\$ or US-20020023255-\$ or US-20050033831-\$ or JP-2004171167-\$ or US-20030236815-\$ or WO-0188696-\$ or WO-0148599-\$ or EP-750255-\$).did.	US-PGPUB; USPAT; DERWENT	OR	OFF	2009/03/12 12:55
L2	2	1 and (abstraction same architecture)	US-PGPUB; USPAT; DERWENT	OR	ON	2009/03/12 12:56
L3	21	1 and architecture	US-PGPUB; USPAT; DERWENT	OR	ON	2009/03/12 12:58
L4	7	1 and architecture	US-PGPUB; USPAT; DERWENT	OR	ON	2009/03/12 12:58
L5	0	multi\$1core and architecture and abstraction	DERWENT	OR	ON	2009/03/12 13:08
L6	62	multi\$1core and (architecture abstraction)	DERWENT	OR	ON	2009/03/12 13:08
L7	5	6 and @ad<"20030912"	DERWENT	OR	ON	2009/03/12 13:08
L8	0	multi\$1core and (((heterogeneous different) near3 architecture) and abstraction).ab.ti.	US-PGPUB; USPAT	OR	ON	2009/03/12 13:10
L9	35	multi\$1core and ((heterogeneous different) near3 architecture) and abstraction	US-PGPUB; USPAT	OR	ON	2009/03/12 13:10

L10	2	9 and @ad< "20030912"	US-PGPUB; USPAT	OR	ON	2009/03/12 13:10
L11	844	((plurality multiple) near3 (cpu processor)) and (multi\$1threaded multi \$1core hyper\$1threaded smt) and abstraction	US-PGPUB; USPAT	OR	ON	2009/03/12 13:17
L12	763	((plurality multiple) near3 (cpu processor)) and (multi\$1threaded multi \$1core hyper\$1threaded smt) and abstraction and architecture	US-PGPUB; USPAT	OR	ON	2009/03/12 13:18
L13	319	12 and @ad< "20030912"	US-PGPUB; USPAT	OR	ON	2009/03/12 13:18
L14	175	13 and ((number amount many) near3 (core threads ((virtual logical) adj (cpu processor))))	US-PGPUB; USPAT	OR	ON	2009/03/12 13:40
L15	1996	((heterogeneous different) near3 architecture) and abstraction	US-PGPUB; USPAT	OR	ON	2009/03/12 13:45
L16	80	14 and 15	US-PGPUB; USPAT	OR	ON	2009/03/12 13:45
L17	49	((heterogeneous different) near3 architecture) with abstraction	US-PGPUB; USPAT	OR	ON	2009/03/12 13:49
L18	0	14 and 17	US-PGPUB; USPAT	OR	ON	2009/03/12 13:49
L19	6152	((plurality multiple) near3 (cpu processor)) and (multi\$1threaded multi \$1core hyper\$1threaded smt)	US-PGPUB; USPAT	OR	ON	2009/03/12 13:49
L20	1	17 and 19	US-PGPUB; USPAT	OR	ON	2009/03/12 13:50
L21	20	17 and @ad< "20030912"	US-PGPUB; USPAT	OR	ON	2009/03/12 13:50
L22	935	(cmt cmp smt) and (multi \$1threaded hyper \$1threaded multi\$1core)	US-PGPUB; USPAT	OR	ON	2009/03/12 13:56
L23	560	22 and ((number amount many) near3 (core threads ((virtual logical) adj (cpu processor))))	US-PGPUB; USPAT	OR	ON	2009/03/12 13:56
L24	194	23 and @ad< "20030912"	US-PGPUB; USPAT	OR	ON	2009/03/12 13:56

L25	101	((cmt cmp) same smt and (multi\$1threaded hyper\$1threaded multi \$1core))	US-PGPUB; USPAT	OR	ON	2009/03/12 14:24
L26	24	24 and 25	US-PGPUB; USPAT	OR	ON	2009/03/12 14:24
L27	61	25 and ((number amount) near3 thread)	US-PGPUB; USPAT	OR	ON	2009/03/12 14:29
L28	17395	((number amount) near3 (thread or (logical near3 entity)))	US-PGPUB; USPAT	OR	ON	2009/03/12 14:31
L29	1676	((plurality multiple) near3 (cpu processor)) and (multi\$1threaded multi \$1core hyper\$1threaded smt) and 28	US-PGPUB; USPAT	OR	ON	2009/03/12 14:31
L30	717	29 and @ad< "20030912"	US-PGPUB; USPAT	OR	ON	2009/03/12 14:31
L31	780	((number amount) near3 (thread or (logical near3 entity))).ab.ti.	US-PGPUB; USPAT	OR	ON	2009/03/12 14:32
L32	47	30 and 31	US-PGPUB; USPAT	OR	ON	2009/03/12 14:32
L33	1	(US-20040199919-\$).did.	US-PGPUB	OR	OFF	2009/03/12 17:16
L34	1	(US-20040268347-\$).did.	US-PGPUB	OR	OFF	2009/03/12 17:16
L37	983	718/1.ccls.	US-PGPUB; USPAT	OR	OFF	2009/03/12 17:45

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